

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: S. SHUKURI  
Application No.: Rule 1.53(b) Divisional of U.S. Patent Application Serial No. 10/400,469, filed March 28, 2003  
Filed: On Even Date Herewith  
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AN A METHOD OF MANUFACTURING THE SAME  
Art Group of Parent: 2818  
Examiner of Parent: Unknown

**INFORMATION DISCLOSURE STATEMENT**  
**UNDER 37 CFR §1.97 & 1.98**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 30, 2004

Sir:

In the matter of the above-identified application, this Information Disclosure Statement is being submitted with the following citation as specified in 37 CFR §1.97(d).

"A copy of any patent, publication or other information listed in an Information Disclosure Statement is not required to be provided if it was previously cited by or submitted to the Office in a prior application, provided that the prior application is properly identified in the statement and relied upon for an earlier filing date under 35 U.S.C. §120."

Applicants are submitting herewith a copy of Form PTO/SB/08A which list documents cited in parent application Serial No. 10/400,469, filed March 28, 2003, the entire disclosure of which is hereby incorporated by reference.

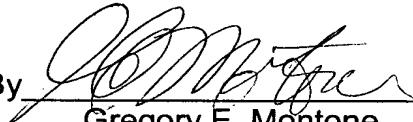
It is respectfully requested that this information disclosure statement be considered by the Examiner.

To the extent necessary, the applicants petition for an extension of time under 37 CFR 1.136. Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to the deposit account of Antonelli, Terry, Stout & Kraus, LLP Deposit Account No. 01-2135 (Docket No. 501.42645VX1).

Respectfully submitted,

ANTONELLI, TERRY, STOUT & KRAUS, LLP

By



Gregory E. Montone  
Reg. No. 28,141

GEM/dlt

1300 North Seventeenth Street, Suite 1800  
Arlington, Virginia 22209  
Telephone: (703) 312-6600  
Facsimile: (703) 312-6666

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Substitute for form 1449A/PTO				<b>Complete if Known</b>	
<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b> <i>(use as many sheets as necessary)</i>				<b>Application Number</b>	Not assigned yet
				<b>Filing Date</b>	On even date herewith
				<b>First Named Inventor</b>	S. SHUKURI
				<b>Art Unit</b>	Not assigned yet
				<b>Examiner Name</b>	Not assigned yet
Sheet	1	of	1	<b>Attorney Docket Number</b>	501.42645VX1

## **U.S. PATENT DOCUMENTS**

## FOREIGN PATENT DOCUMENTS

#### **OTHER PRIOR ART—NON PATENT LITERATURE DOCUMENTS**

EXAMINER'S FINGERPRINT - NON-PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), <small>publisher city and/or country where published</small>	T <sup>2</sup>
		"Can NROM, a 2 Bit, Trapping Storage NVM Cell, Give a Real Challenge to Floating Gate Cells?" Eitan et al., Extended Abstracts of the 1999 International Conference on Solid State Devices and Materials, Tokyo, 1999, pp.522-524	
		"High Speed Program/Erase Sub 100 nm MONOS Memory Cell" Fujiwara et al., pp.75-77	
		"A Novel Flash Memory Device with Split Gate Source Side Injection and ONO Charge Storage Stack (SPIN)" Chen et al., 1997 Symposium on VLSI Technology Digest of Technical Papers, pp. 63-64	
		"Twin MONOS Cell with Dual Control Gates" Hayashi et al., Symposium on VLSI Technology Digest of Technical Papers, pp. 122-123	

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.

**Burden Hour Statement:** This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.